

Amendments to the Claims:

The following claims will replace all prior versions of the claims in this application (in the unlikely event that no claims follow herein, the previously pending claims will remain):

1. (Currently Amended) An apparatus for inspecting errors in a system board including a number of channels for controlling the overall operation of a system, the apparatus comprising:

judging means for judging signals respectively received in the number of channels of the system board;

comparing means for comparing the signals respectively received in the number of channels of the system board through digital operation and finding a failed channel to continuously output a ~~failure-detection first~~ signal of for the failed channel; and

signal-maintaining means for judging signals of the failed channels from the comparing means and carrying out a feedback of a signal that is the same as ~~substantially similar to~~ the signals of other channels to improve stability of system trip;

wherein the judging means is of a MISO type generating a normal command signal removing errors after judging errors by comparing the signals received from a number of channels of the system board through digital operation, and

wherein, if the comparing means determines that the failed channel failed due to an instantaneous noise, the comparing means transmits ~~an initialization~~ a second signal to cancel the ~~failure-detection first~~ signal.

2. (Previously Presented) An apparatus for inspecting errors in a system board according to claim 1, wherein said judging means, comparing means and maintaining means are provided in a single chip.

3. (Original) An apparatus for inspecting errors in a system board according to claim 1, wherein said judging means comprises:

a bypass terminal for separating a signal of the failed channel when interruption of the signal is required due to repair of the failed channel in the system board; and

a manual trip terminal capable of forcibly changing the output value of said judging means to convert the operation.

4. (Original) An apparatus for inspecting errors in a system board according to claim 1, wherein said comparing means compares the input signals in the channels of the system board, and if a channel outputs a signal different from the other channels, outputs a signal informing that the channel outputting the different signal is failed.

5. (Original) An apparatus for inspecting errors in a system board according to claim 1, wherein said maintaining means comprises a logic circuit for transmitting an operation signal simultaneously with the signal outputted from the failed channel in the system board to normally maintain the operation of the system board upon receiving the failed channel informing signals.

6. (Original) An apparatus for inspecting errors in a system board according to claim 5, wherein said logic circuit is an OR gate.

7. (Canceled)

8. (Currently Amended) An apparatus for inspecting errors in a system board including a number of channels for controlling the overall operation of a system, the apparatus comprising:

at least one voter for judging signals respectively received in the number of channels of the system board;

at least one comparator for comparing the signals respectively received in the number of channels of the system board through digital operation and finding a failed channel to continuously output a ~~failure detection~~ a first signal of for the failed channel; and

at least one detector for judging signals of the failed channels from the comparing means and carrying out a feedback of a signal that is ~~substantially similar~~ the same as to the signals of other channels to improve stability of system trip;

wherein the at least one voter is a of a MISO type generating a normal command signal removing errors after judging errors by comparing the signals received from a number of channels of the system board through digital operation, and

wherein, if the comparing means determines that the failed channel failed due to an instantaneous noise, the comparing means transmits ~~an initialization~~ a second signal to cancel the ~~failure detection~~ first signal.

9. (Previously Presented) An apparatus for inspecting errors in a system board according to claim 8, wherein said at least one voter, comparator, and detector are provided in a single chip.

10. (Previously Presented) An apparatus for inspecting errors in a system board according to claim 8, wherein said at least one voter comprises:

a bypass terminal for separating a signal of the failed channel when interruption of the signal is required due to repair of the failed channel in the system board; and

a manual trip terminal capable of forcibly changing the output value of said at least one voter to convert the operation.

11. (Previously Presented) An apparatus for inspecting errors in a system board according to claim 8, wherein said at least one comparator compares the input signals in the channels of the system board, and if a channel outputs a signal different from the other channels, outputs a signal informing that the channel outputting the different signal is failed.

12. (Previously Presented) An apparatus for inspecting errors in a system board according to claim 8, wherein said detector comprises a logic circuit for transmitting an operation signal simultaneously with the signal outputted from the failed channel in the system board to normally maintain the operation of the system board upon receiving the failed channel informing signals.

13. (Previously Presented) An apparatus for inspecting errors in a system board according to claim 12, wherein said logic circuit is an OR gate.